



*Bf*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Confirmation No.: 4758

Nobutaka TANIGUCHI

Art Unit: 2816

Serial No.: 09/777,897

Examiner: L. Nguyen

Filed: February 7, 2001

Docket No.: 100353-00039

For: DELAY TIME ADJUSTING METHOD OF DELAYING A PHASE OF AN  
OUTPUT SIGNAL UNTIL A PHASE DIFFERENCE BETWEEN AN INPUT  
SIGNAL AND THE OUTPUT SIGNAL BECOMES AN INTEGRAL  
NUMBER OF PERIODS OTHER THAN ZERO

**RESPONSE UNDER 37 C.F.R. 1.121**

Mail Stop Amendments  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

August 23, 2004

Sir:

Responsive to the Office Action dated May 21, 2004 (August 21, 2004 falling on  
a Saturday), consideration of the following amendment is respectfully requested.

**IN THE DRAWINGS**

Enclosed please find a proposed corrected drawing sheet including corrections to

FIG. 5.